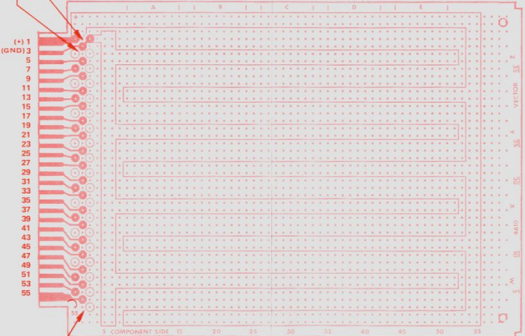


4610 PLUGBOARD™
COMPONENT SIDE

To prevent shorting wrapped wire to etched circuit when wire wrapping, use one or more insulated turns at bottom of wrap post; also, do not chisel cut wire against etched circuit as a shorting burr may occur.

Before pressing terminals into board, position (metal) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.



This link may be cut open to isolate pin 3 from pin 4 for non-STD-BUS use.

In any plug contact area on either side of Plugboard™, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.

(+) 1
(GND) 3

5
7
9
11
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15
17
19
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CONTACT NUMBERS ARE PER STD-BUS CONVENTION.
See notes on wiring side layout paper for bussing pin 1 to 2, & pin 3 to 4 for STD-BUS

VECTOR DIP PLUGBOARD™
PATTERN 0.042" DIA. x 0.1" SPACED HOLES
LA25-P1 LAYOUT PAPER

NOTE:
Where tin-coated circuitry exists, a small percentage of the holes may have solder blockage.

This is a light "saw" easily penetrated by components leads. In some cases, a soldering iron may be required.

APPLICATION:
Intended for use in non-hostile environments up to 200 volts RMS or 300 volts DC.

NOTE:
Circle above edge contacts indicates location of actual connector contact pads on opposite side of board.

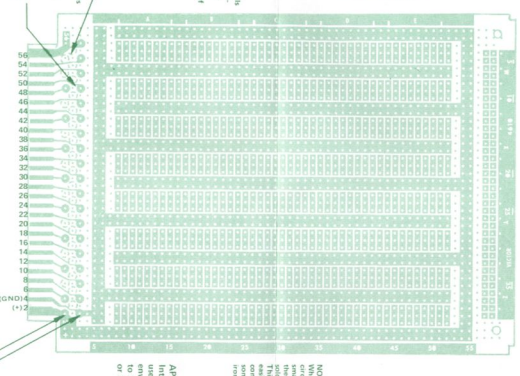
4610 PLUGBOARD™
WIRING SIDE

To prevent shorting wrapped wire to etched circuit when wire wrapping, use one or more insulated turns at bottom of wrap post; also, do not chisel-cut wire against etched circuit as a shorting burr may occur.

Before pressing terminals into board, position (rotate) terminals to maximize the clearance between the widest part of the terminal and the nearest adjacent conductor.

NOTE: Broken circles above edge contacts indicate location of actual connector contact pins on opposite side of board.

In any plugging operation on either side of Plugboard, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.



NOTICE:
Where tin-coated circuitry exists, a small percentage of the holes may have solder blockage. This is a light "skin" easily generated by components leads. In some cases, a soldering iron may be required.

APPLICATION:
Intended for use in non-hostile environments up to 200 volts RMS or 300 volts DC.

VECTOR DIP PLUGBOARD™
PATTERN 0.042" DIA. x 0.10" SPACED HOLES.
LA25-P2 LAYOUT PAPER